



- UL recognition, file #E313149
- Glass passivated chip junction
- Ideal for automated placement
- High surge current capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

General purpose use in AC/DC bridge full wave rectification for SMPS, lighting ballaster, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

- : YBS3
Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, Halogen-free
- : Tin plated leads, solderable per J-STD-002 and JESD22-B102
- As marked on body

■ (Ta=25 Unless otherwise specified)

Device marking code			RYBSM6010
Maximum Repetitive Peak Reverse Voltage	VRRM	V	1000
Maximum RMS Voltage	VRMS	V	700
Maximum DC blocking Voltage	VDC	V	1000
Average rectified output current @60Hz sine wave, R-load, Tc=60	IO	A	6.0
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, Tj=25	IFSM	A	150
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25			300
Current squared time @1ms≤t≤8.3ms Tj=25 Rating of per diode	I ² t	A ² s	93.4
Storage temperature	Tstg		-55 ~ +150
Junction temperature	Tj		-55 ~ +150

■ Ta=25 Unless otherwise specified



■ $T_a=25$ Unless otherwise specified

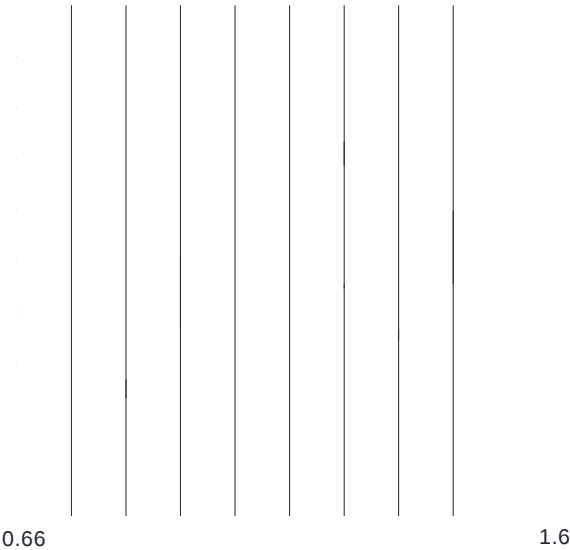
Typical Thermal Resistance	Between Junction and Ambient	$R_{\theta J-A}$	/W	55
	Between Junction and Lead	$R_{\theta J-L}$		14
	Between Junction and Case	$R_{\theta J-C}$		8

Note: Device mounted on P.C.B with 35mm*25mm*1.7mm.

■ (Example)

RYBSM6010	F1	Approximate 0.36	1800	3600	25200	13" Reel

■ (Typical)





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A	10.00	10.40
B	9.70	10.10



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