



UL recognition, file #E230084
Glass passivated chip junction

rating, RoHS compliant, halogen free
: Tin plated leads, solderable per
J-STD-002 and JESD22-B102
As marked on body

($T_a=25$ Unless otherwise specified)

Device marking code			PB5008L	
Maximum Repetitive Peak Reverse Voltage	VRRM	V	800	
Maximum RMS Voltage	VRMS	V	560	
Maximum DC blocking Voltage	VDC	V	800	
Average rectified output current @60Hz sine wave, R-load	With heatsink $T_c=92$	IO	A	50.0
	Without heatsink $T_a=25$			4.5
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, $T_j=25$	IFSM	A	500	
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, $T_j=25$			1000	
Current squared time @1ms t 8.3ms $T_j=25$, Rating of per diode	I^2t	A^2s	1037.5	
Storage temperature	T_{stg}		-55 ~ +150	
Junction temperature	T_j		-55 ~ +150	
Dielectric strength @ terminals to case, AC 1 minute	V_{dis}	KV	2.5	
Mounting torque @ recommend torque 5kg cm	T_{or}	kg cm	8	

$T_a=25$ Unless otherwise specified

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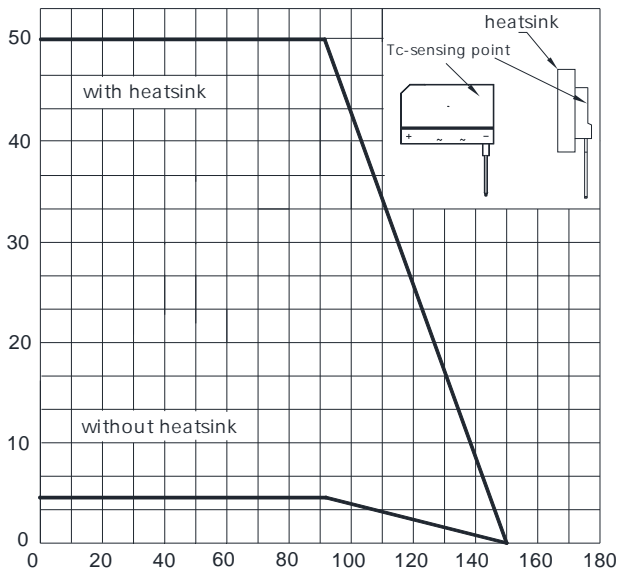
Thermal Resistance	Between junction and ambient, Without heatsink	R_{J-A}	/W	18.0
	Between junction and case, With heatsink	R_{J-C}		0.6

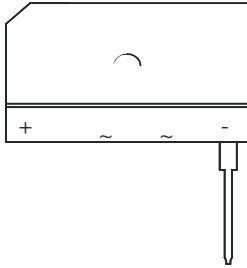
Note: Device mounted on 75mm x 45mm x 5.5mm Aluminum Plate Heatsink.

(Example)

PB5008L	B1	Approximate 7.5	15	750	1500	TUBE

(Typical)





Dimensions in millimeters

A	29.7	30.3
B	19.7	20.3
C	17.0	18.0
D	4.8	5.8
E	3.8	4.2
F	7.3	7.7
G	9.8	10.2
H	0.9	1.1
I	2.0	2.4
J	2.3	2.7
K	3.4	3.8
L	4.4	4.8
M	10.8	11.2
N	3.1	3.7
O	3.1	3.4
P	0.6	0.8
A1	28.75	29.15
B1	18.75	19.15
C1	0.3	0.7
D1	0.3	0.7



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