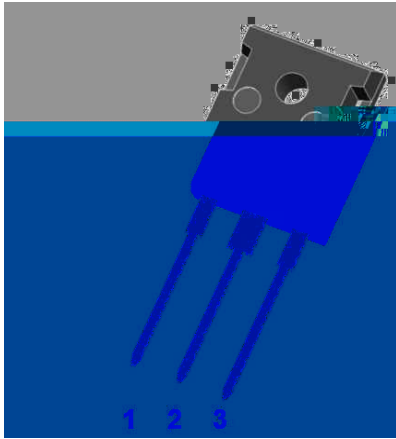


- High frequency operation
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Gu Tin plated leads, solderable per

J-STD-002 and JESD22-B102

- : As marked

($T_a=25$ Unless otherwise specified)

Device marking code			MBR40200PT
Repetitive Peak Reverse Voltage	V_{RRM}	V	200
Average Rectified Output Current @60Hz sine wave, R-load, $T_c=139$	I_O	A	40
Surge(Non-repetitive)Forward Current @60Hz half sine-wave, 1 cycle, $T_a=25$	I_{FSM}	A	300
Current Squared Time @ $1ms \leq t \leq 8.3ms$ $T_j=25$	I^2t	A^2s	373
Storage Temperature	T_{stg}		-55 ~ +175
Junction Temperature	T_j		-55 ~ +175

Peak Forward Voltage	V _{FM}	V	I _{FM} =20.0A T _j =25	0.5	0.85	0.9
			I _{FM} =20.0A T _j =125	-	0.74	0.78
DC reverse current at rated DC blocking voltage per diode	I _{RRM1}	mA	V _{RM} =V _{RRM}	+	+	
Junction capacitance	C _j	pF	1MHz and Applied Reverse Voltage of 4.0 V.D.C	200	320	550

Note1:Pulse test:300uS pulse width,1% duty cycle

Note2:Pulse test:pulse width 40mS



■ Ta=25 Unless otherwise specified				
Thermal Resistance	Between junction and ambient	R _{θJ-A}	/W	50.0
	Between junction and case	R _{θJ-C}		



■

Dim	Min	Max
A	4.80	5.20
A1	2.21	2.61
A2	1.85	2.15
b	1.0	1.4
b2	1.91	2.21
C	0.5	0.7
D	20.70	21.30
D1	16.25	16.85
E	15.50	16.10
E1	13.0	13.6
E2	4.80	5.20
E3	2.30	2.70
L	19.62	20.22
L1	-	4.30
ΦP	3.40	3.80
ΦP1	-	7.30
S	6.15TYP	
H1	5.44TYP	
b3	2.80	3.20



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