



Dual NPN Digital Transistors (Built-in Resistors)

Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Mechanical Data

- **Package:** SOT-363
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** H3

■Equivalent circuit

■ Characteristics (Typical)

Fig. 1 - Static Characteristics.

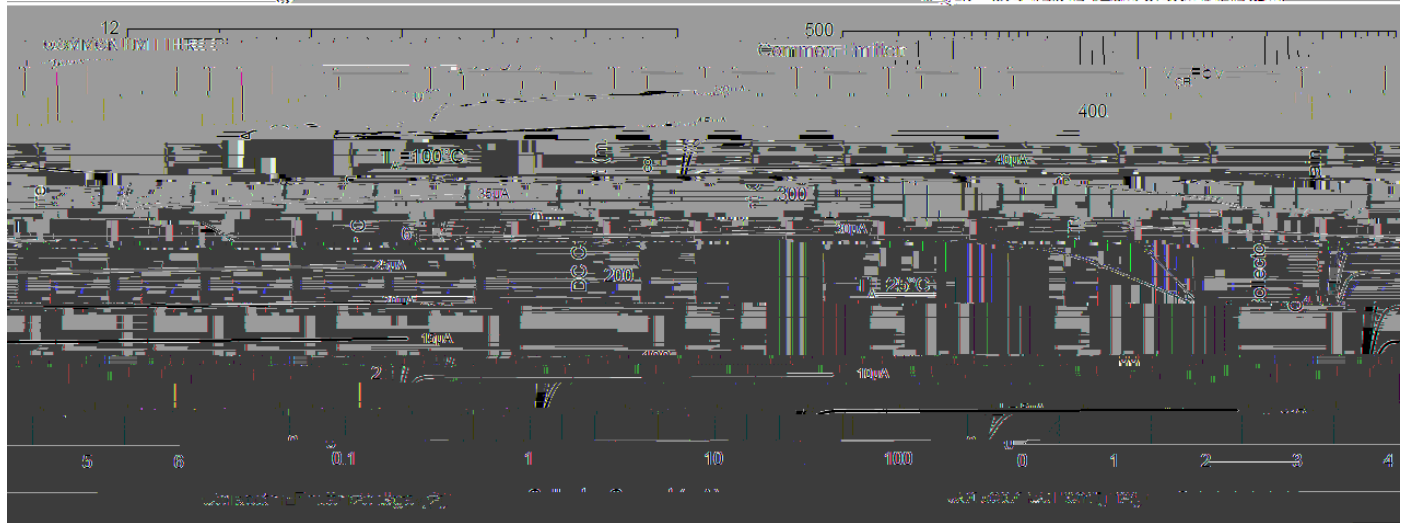


Fig. 2 - DC Current Gain Characteristics.

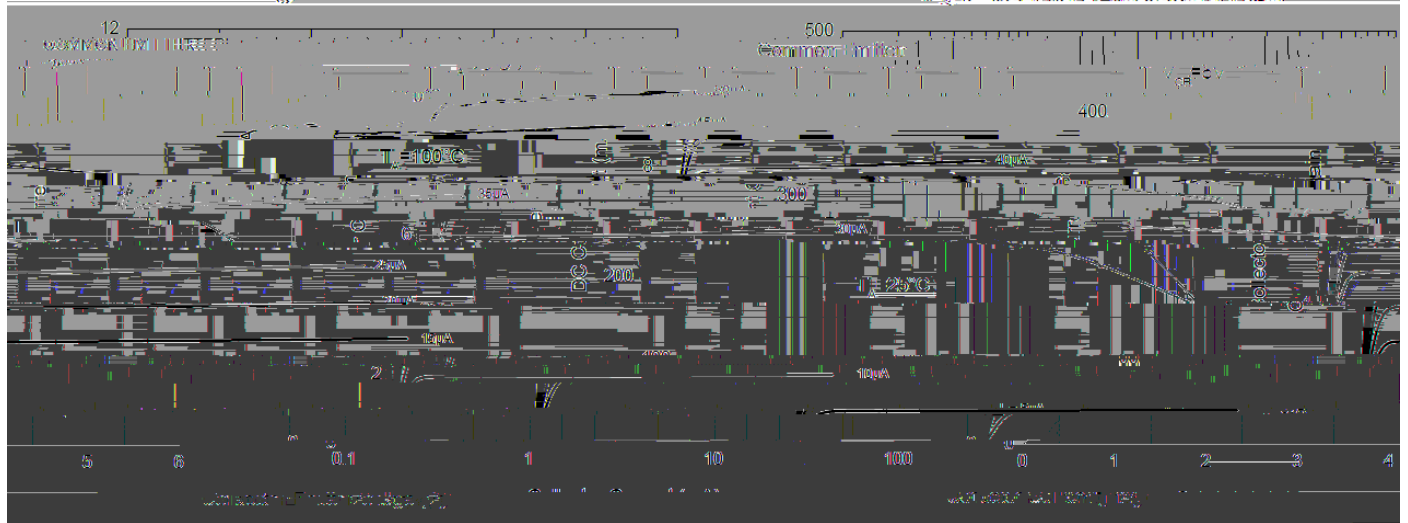
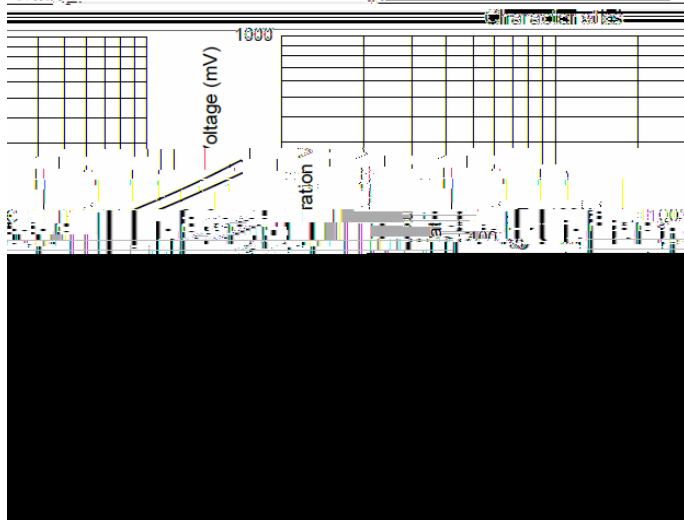
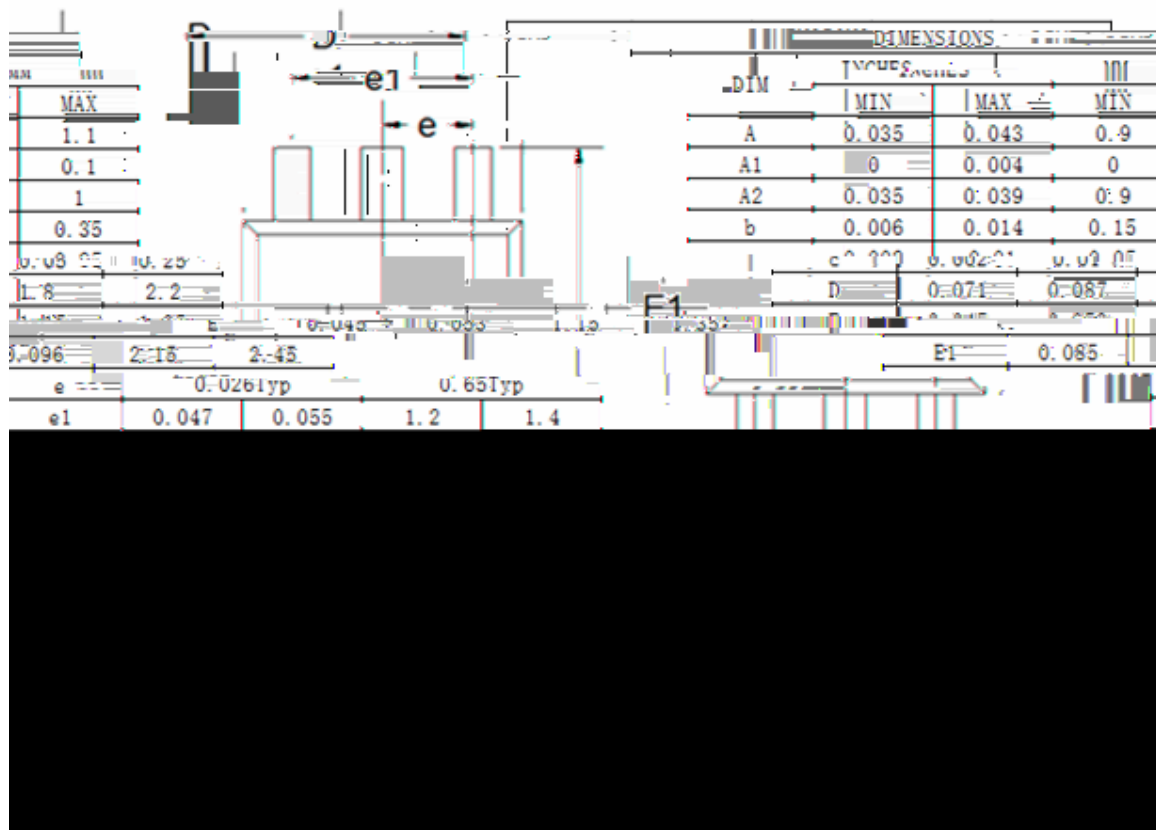


Fig. 3 - Collector-Emitter Saturation Voltage.





■SOT-363 Package Outline Dimensions



■SOT-363 封装尺寸

