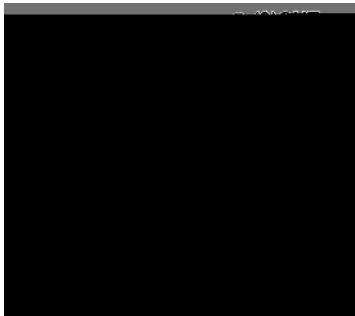




DTA124XCA



PNP Digital Transistors (Built-in Resistors)



Features

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- Pæ|[*^}Ä-!^^Áæ}áÜ[PÜÁ&{ }|äæ}cÁ
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Application

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- Ü , äc&@ä} *Á&ä!&~äcÁÁ

Mechanical data

- Package Á ÜUVËGH
- Terminals Vâ}Á]|æc^áÁ|^æâ•ÉÁ•[|ä^!æä|^ÁÁ]^!
- RËÜVÖË€€GÁæ}áÁRÒÜÖGGËÓF€G

Maximum Ratings

Item	Symbol	Unit	Conditions	Value
Input voltage	V_i	V		-4 \$ to Ž 1 \$
Output current	I_o	mA		-1 \$ \$
Power dissipation	P_D	mK		2 \$ \$
> unction temperature	T_j	°C	°C	Éí íÁc[ÁÉFíÉÁ
Üc[!æ*^Äc^ { } ^!æc~!^ÁÁ	$V_{ÜVÖÁ}$	°C	°C	Éí íÁc[ÁÉFíÉÁ

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DTA124XCA

RoHS
COMPLIANT

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Ü^çÈFÈFÈ€JÈRæ}ÈGÎÁ

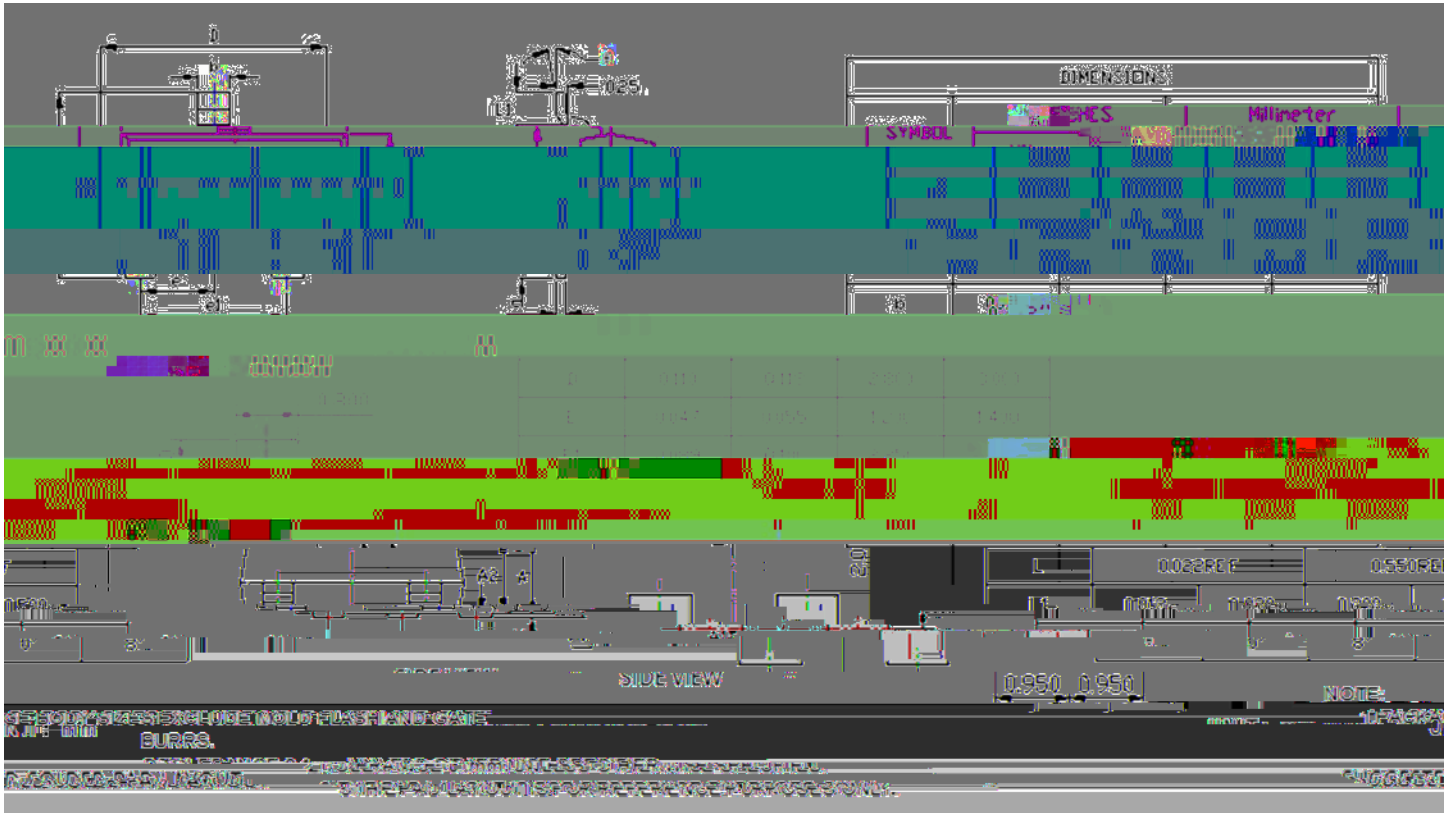
Ÿæ} * :@ [^ÁŸæ} *bâ^ÁÒ|^&c! [] â&ÁV^&@} [[[* ^ ÁÔ [ÈÈÁŠcâÈÁ , , , ÈGF^æ} *bâ^È& [{



DTA124XCA

RoHS
COMPLIANT

Outline Dimensions



Note:

1. All dimensions are in millimeters (mm) unless otherwise specified.

All dimensions shown are exclusive of burrs and gate residues. Burrs and gate vestiges shall not exceed 0.15 mm in maximum.

所有尺寸均不包括毛刺和浇口残留。毛刺和浇口残留不得超过0.15毫米。

Dimensions are shown and every dimensioned surface of the package is subject to mold mismatch.

尺寸均包括在图中，且每个标注尺寸的表面均可能因模具不匹配而产生偏差。

Formed leads shall be planar with respect to one another within a maximum of 0.076 mm relative to the seating plane.

成型引线应在最大0.076毫米的范围内相对于安装平面保持共面。

★ It is the key size.

★ 标注为关键尺寸

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