



MoistureÆ r s s o P ψls

Signal amplification
Switching circuit

J-STD-002 and JESD22-B102

■ (Ta=25 unless otherwise noted)

		8 Q L W	& R Q G L W L R Q V	9 D O X H
Device marking code				619L
Collector-base voltage	V _{CBO}	V	I _C =100μA,I _E =0	50
Collector-emitter voltage	V _{CEO}	V	I _C =10mA,I _B =0	50
Emitter-base voltage	V _{EBO}	V	I _E =100μA,I _C =0	5
Collector current	I _C	A		2
Power dissipation	P _D	mW		200
Junction temperature	T _J			-55 to +150
Storage temperature	T _{STG}			-55 to +150

■ (Ta=25 unless otherwise noted)

Collector-base breakdown voltage	V _{CBO}	V	I _C =100μA,I _E =0	50		
Collector-emitter breakdown voltage	V _{CEO}	V	I _C =10mA,I _B =0	50		
Emitter-base breakdown voltage	V _{EBO}	V	I _E =100μA,I _C =0	5		
Collector-base cut-off current	I _{CBO}	uA	V _{CB} =40 V ,I _E =0			0.1
Emitter-base cut-off current	I _{EBO}	uA	V _{EB} =4 V , I _C =0			0.1
DC current gain	h _{FE1}		V _{CE} =2V,I _C =10mA	200		
	h _{FE2}		V _{CE} =2V,I _C =200mA	300		
	h _{FE3}		V _{CE} =2V,I _C =1A	200		
	h _{FE4}		V _{CE} =2V,I _C =2A	100	Tin plated leads, solderable p	



Collector-emitter saturation voltage	$V_{CE(sat)1}$	V	$I_C=100mA, I_B=10mA$			0.02
	$V_{CE(sat)2}$	V	$I_C=1A, I_B=10mA$			0.2
	$V_{CE(sat)3}$	V	$I_C=2A, I_B=100mA$			0.22
Base-emitter saturation voltage	$V_{BE(sat)*}$	V	$I_C=2A, I_B=50mA$			1
Base-emitter voltage	V_{BE}	V	$V_{CE}=2V, I_C=2A$			1
Transition frequency	f_T	MHz	$V_{CE}=5V, I_C=100mA, f=100MHz$	100		
Collector-base output capacitance	C_{ob}	pF	$V_{CB}=10V, I_E=0, f=1MHz$			20



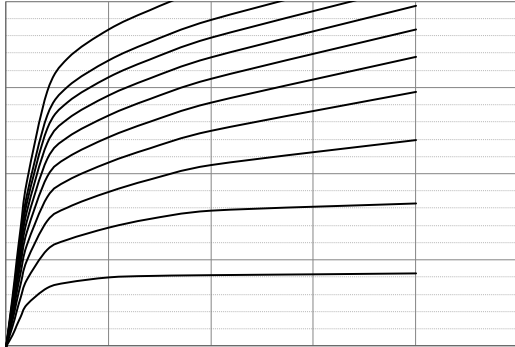
Thermal resistance, junction-to-ambient	$R_{\theta J-A}^{(1)}$	/W	625
Thermal resistance, junction-to-case	$R_{\theta J-C}^{(1)}$	/W	500



(Example)

FMMT619L	F2	Approximate 0.014	3000	30000	120000	7" reel

(Typical)







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