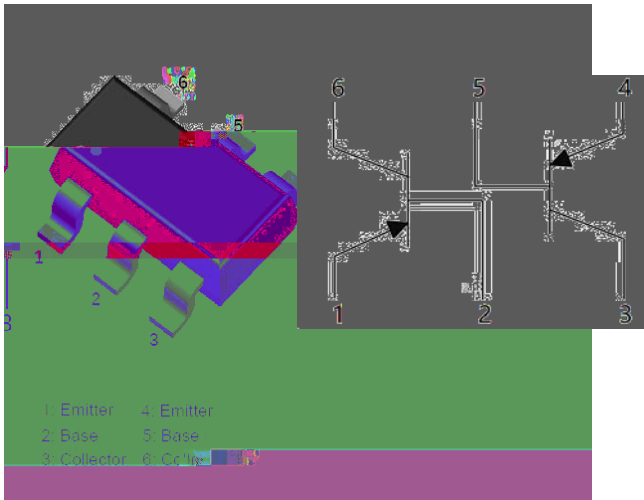




Dual PNP Small Signal Transistor



Features

- z Moisture sensitivity level 1
- z Halogen free and RoHS compliant
- z Surface mount package ideally suited for automatic Insertion

Application

- z Signal amplification
- z Switching circuit

Mechanical data

- z **Package** SOT-23-6L
- z **Terminals** Tin plated leads, solderable per J-STD-002 and JESD22-B102

■ Maximum Ratings (Ta=25 Unless otherwise specified)

	S y m b o l	U n i t	C o n d i t i o n s	V a l u e
Device marking code				5Ft
Collector-base voltage	V_{CBO}	V	$I_C = -10\mu A, I_E = 0$	-80
Collector-emitter voltage	V_{CEO}	V	$I_C = -10mA, I_B = 0$	-65
Emitter-base voltage	V_{EBO}	V	$I_E = -10\mu A, I_C = 0$	-5
Collector current	I_C	mA		-100
Power dissipation	P_D	mW		200
Operation junction temperature	T_J	-		-55 to +150
Storage temperature	T_{STG}	-		-55 to +150



BC856DS

0”

Item	Symbol	Unit	Conditions	Min	Typ	Max
Collector-base breakdown voltage	$V_{(BR)CBO}$	V	$I_C=-10\mu A, I_E$			



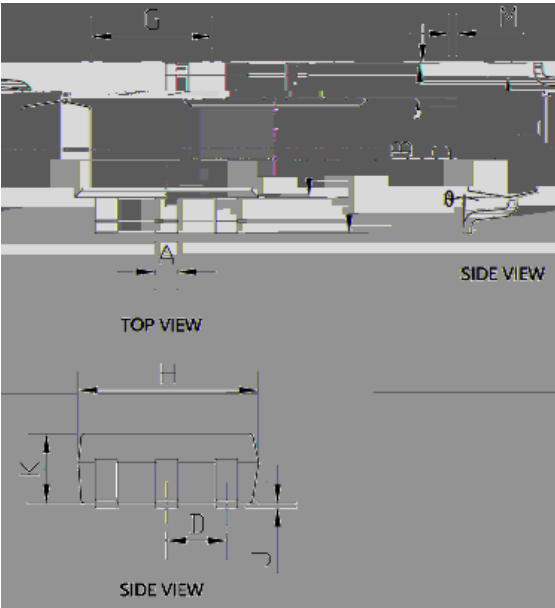


BC856DS

■ Ordering Information

Prefered P/N	Packing Code	Unit weight(mg)	Minimum package(pcs)	Inner box quantity(pcs)	Outer carton quantity(pcs)	Delivery mode
BC856DS	F2	Approximate 15.6	3000	30000	120000	7" reel

■ Outline Dimensions



DIMENSIONS				
SYMBOL	INCHES		MILLIMETER	
	MIN.	MAX.	MIN.	MAX.
A	0.012	0.020	0.300	0.500
B	0.059	0.067	1.500	1.700
C	0.012	0.016	0.300	0.400
D	0.007	0.008	0.175	0.200
E	0.007	0.008	0.175	0.200
F	0.012	0.016	0.300	0.400
G	0.012	0.016	0.300	0.400
H	0.012	0.016	0.300	0.400
I	0.012	0.016	0.300	0.400
J	0.012	0.016	0.300	0.400
K	0.012	0.016	0.300	0.400
L	0.012	0.016	0.300	0.400
M	0.012	0.016	0.300	0.400
N	0.012	0.016	0.300	0.400
O	0.012	0.016	0.300	0.400
P	0.012	0.016	0.300	0.400
Q	0.012	0.016	0.300	0.400
R	0.012	0.016	0.300	0.400
S	0.012	0.016	0.300	0.400
T	0.012	0.016	0.300	0.400
U	0.012	0.016	0.300	0.400
V	0.012	0.016	0.300	0.400
W	0.012	0.016	0.300	0.400
X	0.012	0.016	0.300	0.400
Y	0.012	0.016	0.300	0.400
Z	0.012	0.016	0.300	0.400

■ Suggested Pad Layout

