

1-Line, Bi-directional, Ultra-low Capacitance Transient Voltage Suppressor

Features

- " Stand-off voltage: 3.3V Max
- " Transient protection for each line according to IEC61000-4-2(ESD): $\pm 30\text{kV}$ (contact)
- IEC61000-4-5(surge): 4A (8/20 μs)
- " Ultra-low capacitance: $C_J = 0.35\text{pF}$ typ
- " Ultra-low leakage current: $I_R < 1\text{nA}$ typ
- " Low clamping voltage: $V_{CL} = 9\text{V}$ typ. @ $I_{PP} = 16\text{A}$ (TLP)
- "

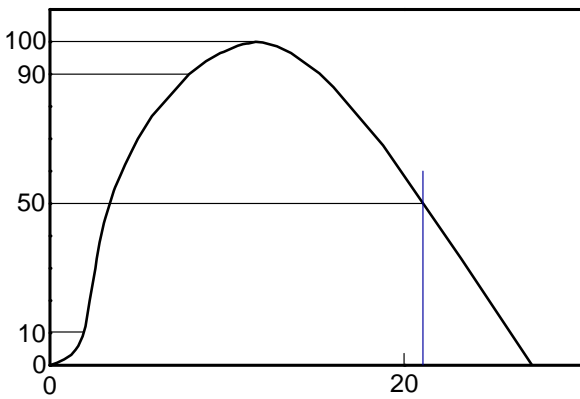
ESDSL3C3V3LZB

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vCharacteristics (Typical)

8/20 μ s waveform per IEC61000 # 5

Contact discharge current waveform per IEC61000 # 2



Clamping voltage vs. Peak pulse current

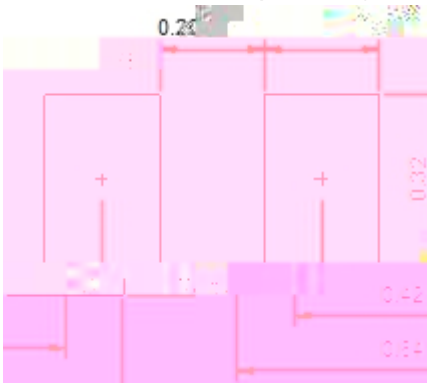
Capacitance vs. Reverse voltage

Non repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature

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Recommend land pattern (Unit:mm)



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met

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