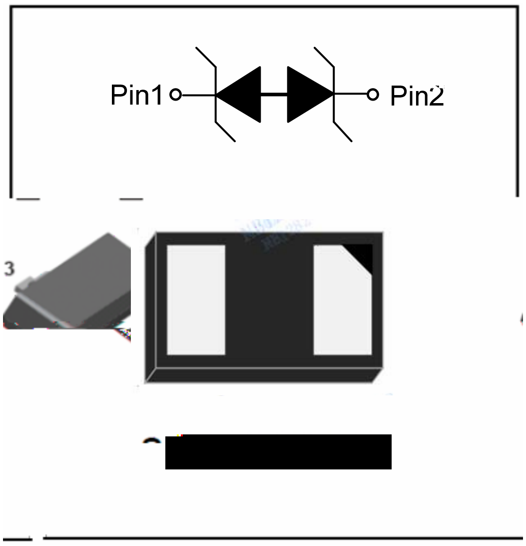
1-Line, Bi-directional, Transient Voltage Suppressor



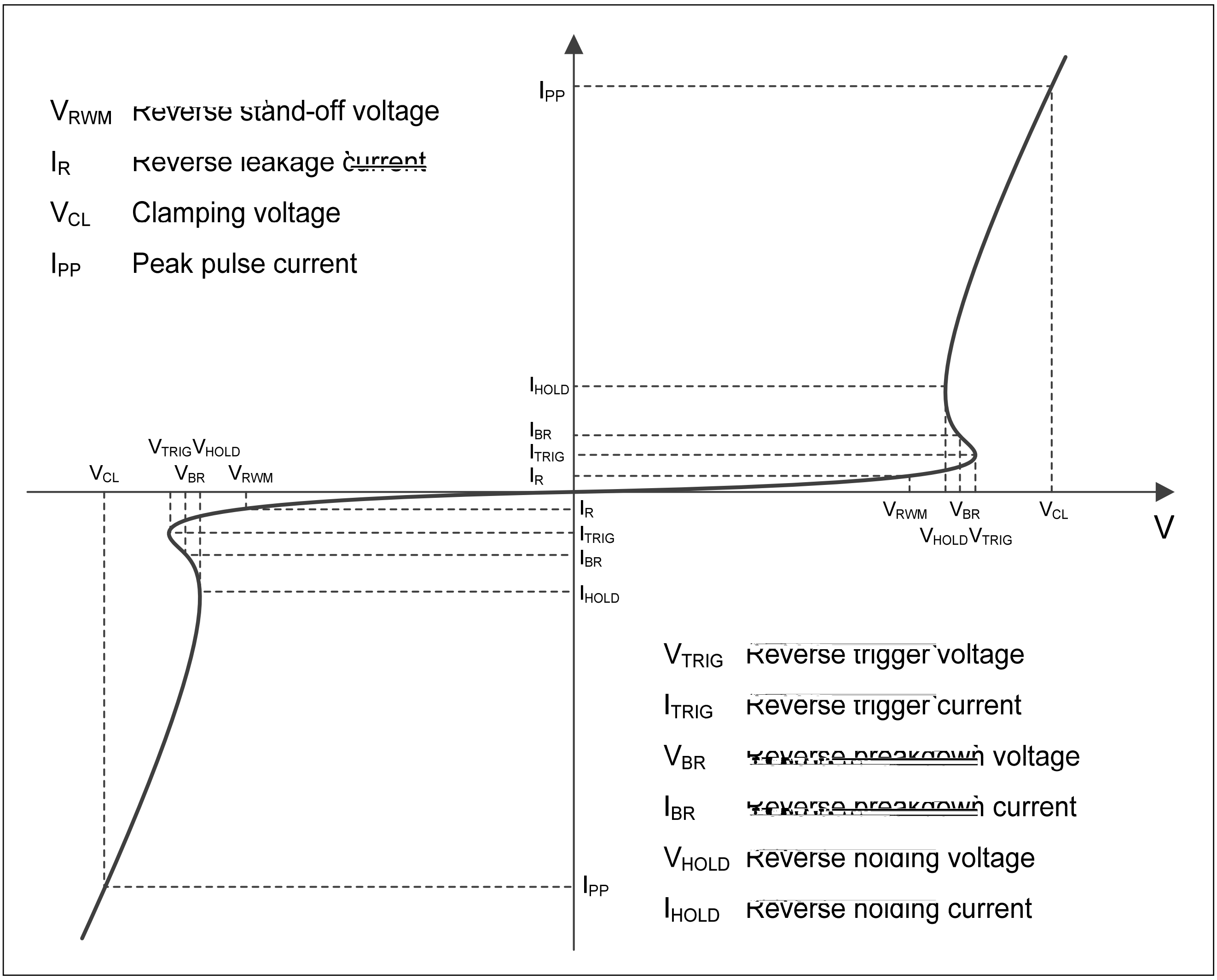
Features

- Stand-off voltage: ±5V Max
- Transient protection for each line according to
IEC61000-4-2(ESD): ±30kV (contact)
IEC61000-4-4 (EFT): 40A (5/50ns)
IEC61000-4-5(surge): 8A (8/20μs)
- Ultra-low capacitance: C_J = 10pF typ
- Low leakage current
- Low clamping voltage: V_{CL} =10.0V typ. @ I_{PP} = 16A (TLP)
- Solid-state silicon technology

Mechanical Data

- **Package**: DFN1006-2L
- **Terminals**: Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** No marking on bi-directional types
- **Marking:** 5A

■Definitions of electrical characteristics

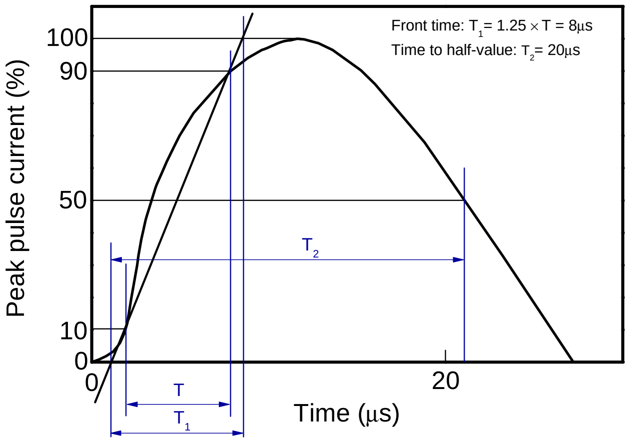


V_{RWM}	Reverse stand-off voltage
I_R	Reverse leakage current
V_{CL}	Clamping voltage
I_{PP}	Peak pulse current

V_{TRIG}	Reverse trigger voltage
I_{TRIG}	Reverse trigger current
V_{BR}	Reverse breakdown voltage
I_{BR}	Reverse breakdown current
V_{HOLD}	Reverse holding voltage
I_{HOLD}	Reverse holding current

■ Characteristics (Typical)

8/20 Îs waveform per IEC61000-4-5



Contact discharge current waveform per IEC61000-4-2

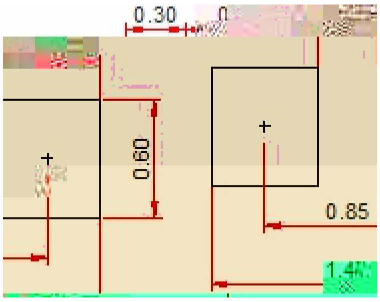
Clamping voltage vs. Peak pulse current

Capacitance vs. Reverse voltage

Non-repetitive peak pulse power vs. Pulse time

Power derating vs. Ambient temperature

■ Recommend land pattern (Unit:mm)



Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met

Disclaimer

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